

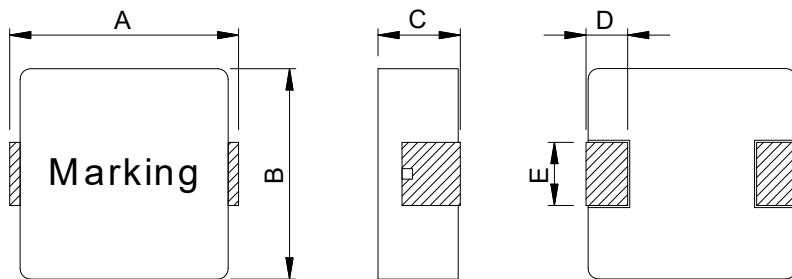
Features

- * Carbonyl Powder.
- * Compact design.
- * High current, low DCR, high efficiency.
- * Very low acoustic noise and very low leakage flux noise.
- * High reliability.
- * 100% Lead(Pb)-Free and RoHS compliant.
- * AEC-Q200 qualified
- * Operating temperature: -55 to +125 °C (including self-temperature rise)

Applications

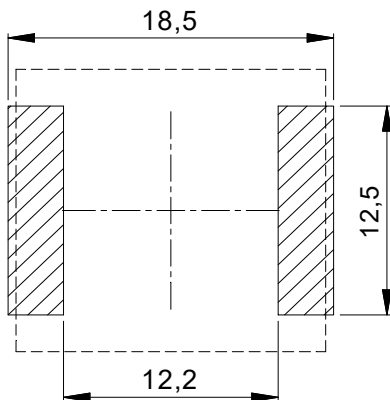
- * Note PC power system, incl. IMVP-6
- * DC/DC converter

Appearance and Dimensions



Dimensions(mm)					
Series	A	B	C	D	E
VMDC-1770	17.60±0.40	16.90±0.30	6.70±0.30	2.50±0.50	11.90±0.30

Land Pattern Dimensions



Circuit Diagram



Product Identification

VMDC - 1770 - 1R0 M

① ② ③ ④

- ① Product Symbol
- ② Dimensions
- ③ Inductance Value (1R0:1.0μH; 100:10μH; 101:100μH)
- ④ Inductance Tolerance (M:20% ; N:30%)

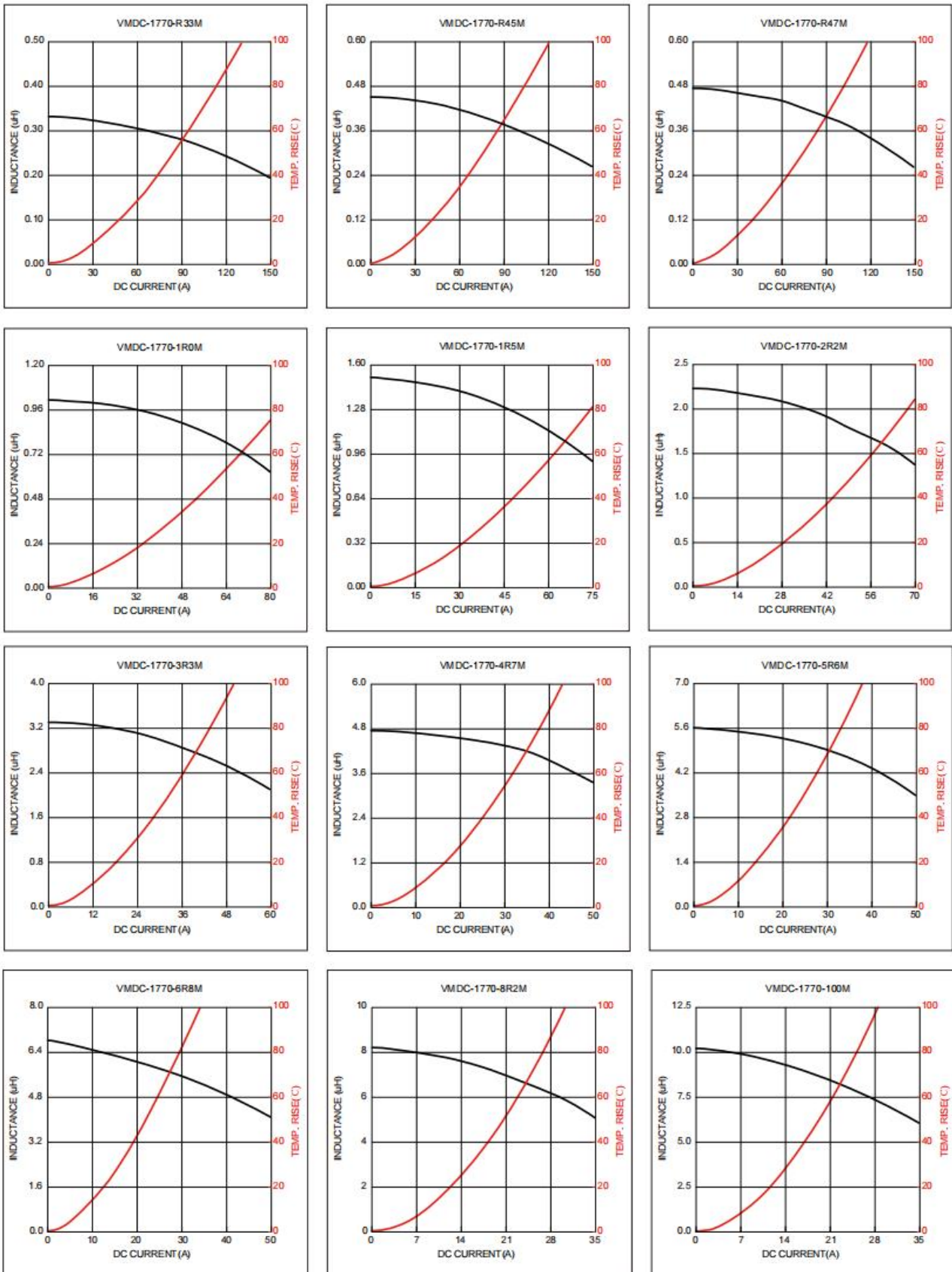
Electrical Characteristics

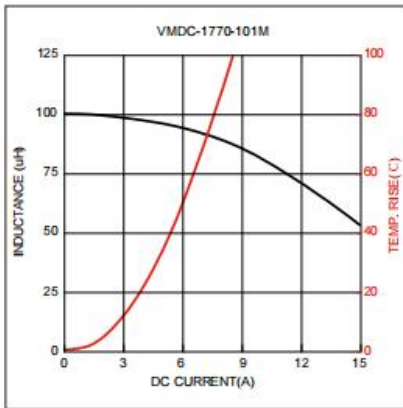
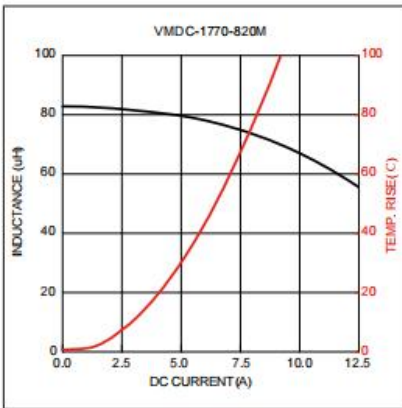
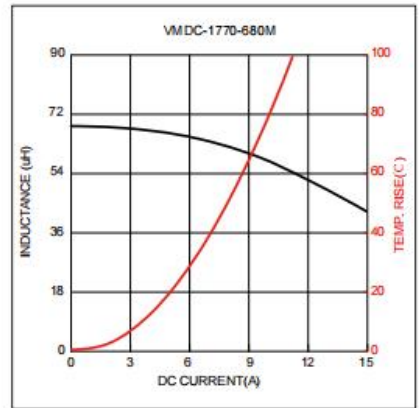
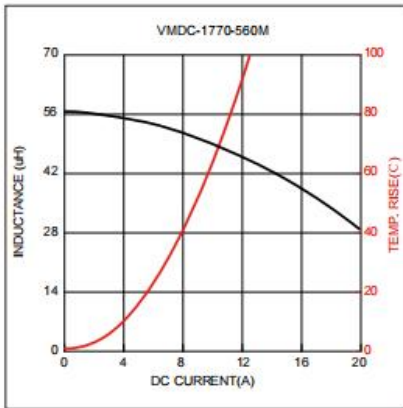
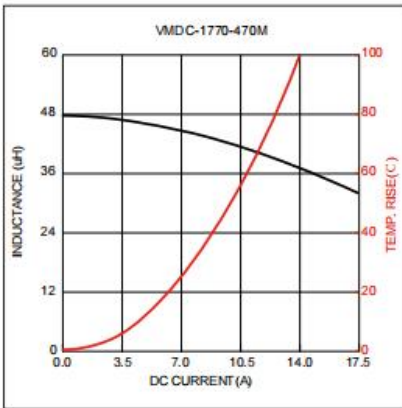
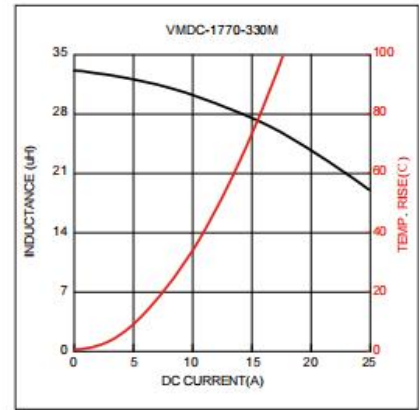
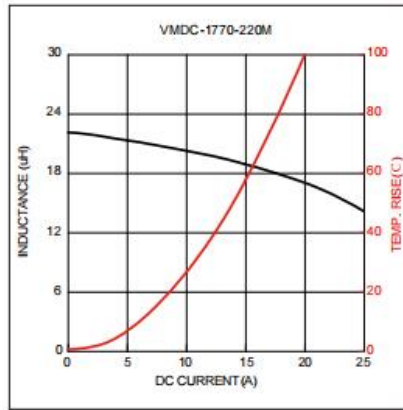
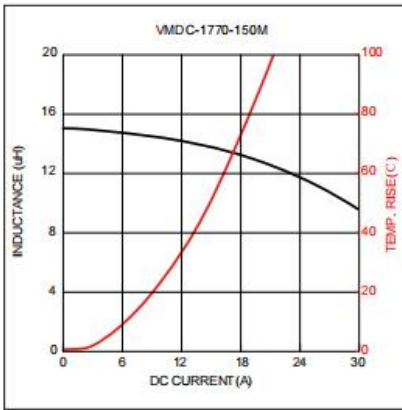
Part No.	Inductance @100KHz/1V	Tolerance	DC Resistance		Saturation Current	Heat Rating Current
			max.	typ.	typ.	typ.
Unit	μH	%	mΩ		A	A
Symbol	L	Tol.	R _{DC}		I _{SAT}	I _{RMS}
VMDC-1770-R33M	0.33	±20	0.90	0.75	130	70.0
VMDC-1770-R45M	0.45	±20	0.96	0.80	125	62.0
VMDC-1770-R47M	0.47	±20	1.03	0.95	123	62.0
VMDC-1770-1R0M	1.00	±20	2.00	1.60	70.0	52.0
VMDC-1770-1R5M	1.50	±20	2.50	2.00	65.0	47.0
VMDC-1770-2R2M	2.20	±20	2.70	2.40	62.0	43.5
VMDC-1770-3R3M	3.30	±20	3.90	3.50	54.0	28.0
VMDC-1770-4R7M	4.70	±20	5.50	4.80	50.0	25.0
VMDC-1770-5R6M	5.60	±20	7.05	5.80	45.0	21.0
VMDC-1770-6R8M	6.80	±20	9.20	8.40	39.0	19.0
VMDC-1770-8R2M	8.20	±20	10.8	9.60	31.0	18.0
VMDC-1770-100M	10.0	±20	13.0	11.8	29.0	16.5
VMDC-1770-150M	15.0	±20	20.5	17.8	27.0	12.5
VMDC-1770-220M	22.0	±20	26.5	25.1	23.0	12.0
VMDC-1770-330M	33.0	±20	44.0	38.0	20.0	10.7
VMDC-1770-470M	47.0	±20	55.0	48.0	16.0	8.70
VMDC-1770-560M	56.0	±20	62.0	54.0	15.0	7.80
VMDC-1770-680M	68.0	±20	80.0	68.0	13.0	7.00
VMDC-1770-820M	82.0	±20	100	87.0	12.0	5.70
VMDC-1770-101M	100	±20	118	102	12.0	5.30

Remark:

- ※1:All test data is referenced to 20°C ambient.
- ※2: R_{DC} :DC resistance at 20°C.
- ※3:Rated current: I_{SAT} or I_{RMS} , whichever is smaller.
- ※4:Saturation Current: typ. Value, DC current at which the inductance drops 30% from its value without current.
- ※5:Heat Rating Current: DC current that causes the temperature rise (ΔT) from 20°C ambient,
for typ. Value, ΔT is approximate 40°C.
- ※6:Specifications subject to change without notice. Please check our website for latest information.

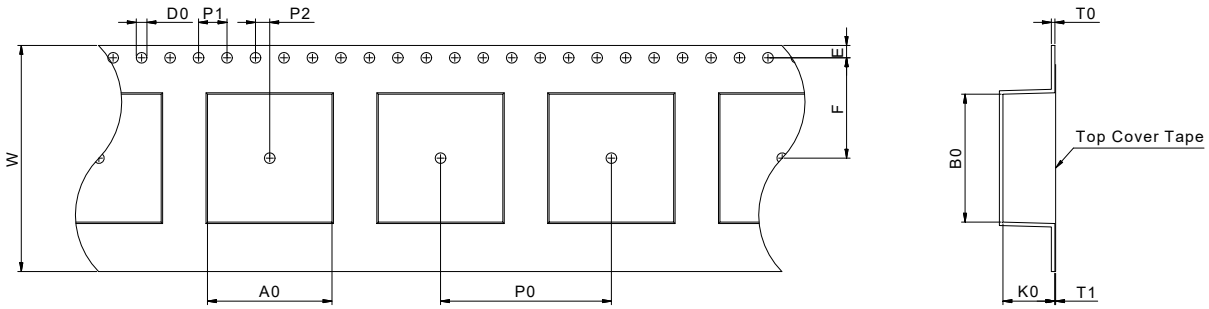
Typical Electrical Characteristics





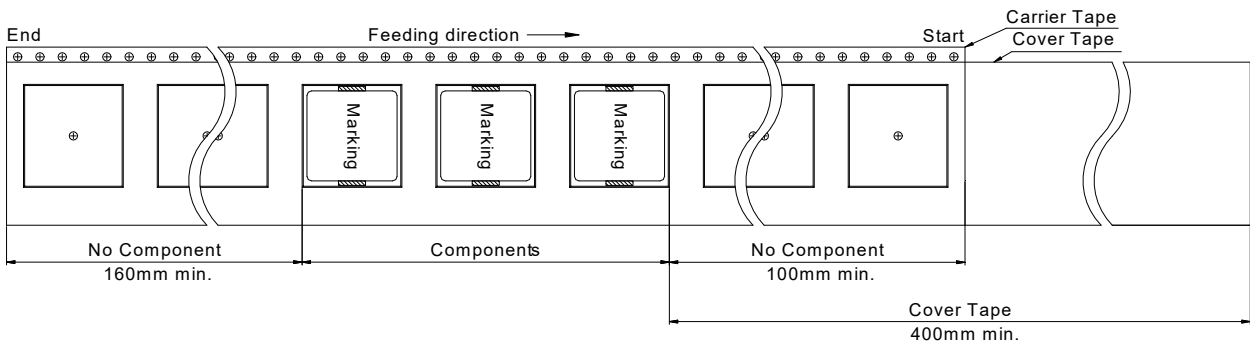
Packing specification

Carrier tape dimensions (mm)



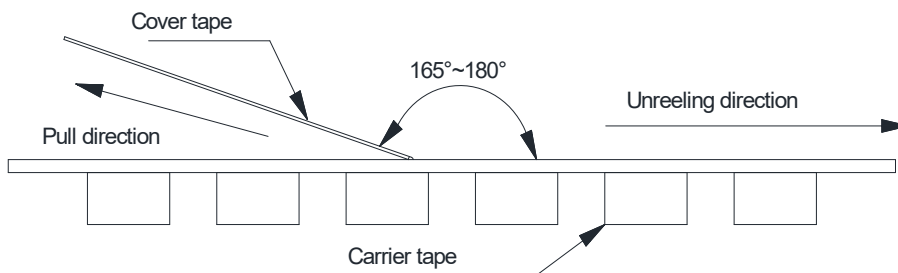
ITEM	W	P0	E	F	T0	T1	A0	B0	K0	D0	P1	P2
Size(mm)	32.00	24.00	1.75	14.20	0.50	0.10	17.50	18.10	7.30	1.50	4.00	2.00
Tolerance	±0.30	±0.10	±0.10	±0.10	ref.	max.	typ.	typ.	typ.	±0.10	±0.10	±0.10

Taping dimensions (mm)

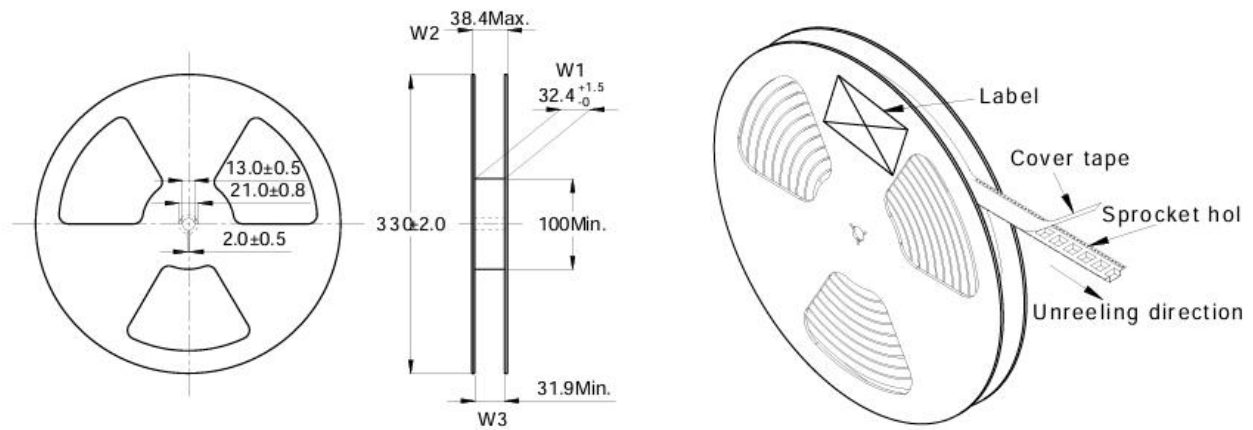


Cover Tape Peel Off Condition

Cover tape peel force shall be 10g to 130g.

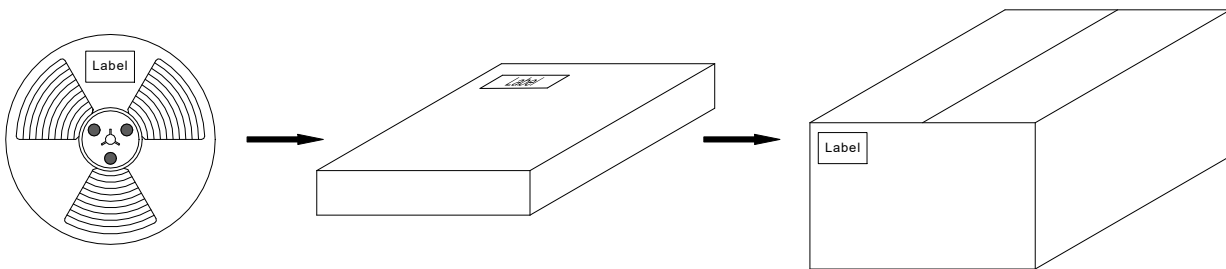


Reel Dimensions (mm)



Packing Quantity

Product Series	Quantity/Reel	Inner Carton Quantity	Outer Carton Quantity
VMDC-1770	200pcs	(200X1)=200pcs	(200X3)=600pcs



Recommended Soldering Conditions

Product can be applied to flow and reflow soldering.

(1) Flux, Solder

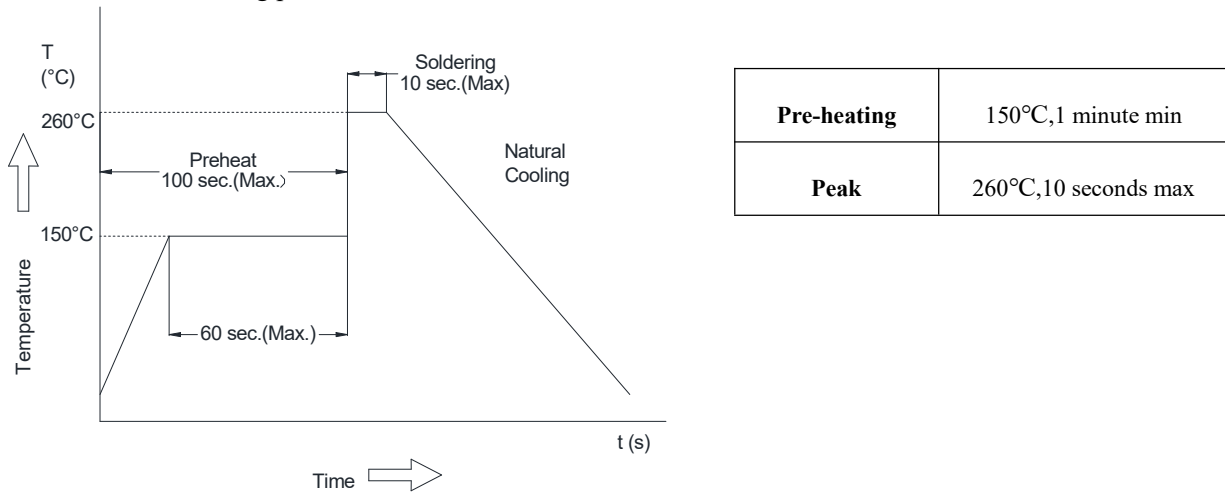
① Use rosin-based flux. Don't use highly acidic flux with halide content exceeding 0.2wt% (chlorine conversion value).

② Use Sn solder.

(2) Flow soldering conditions

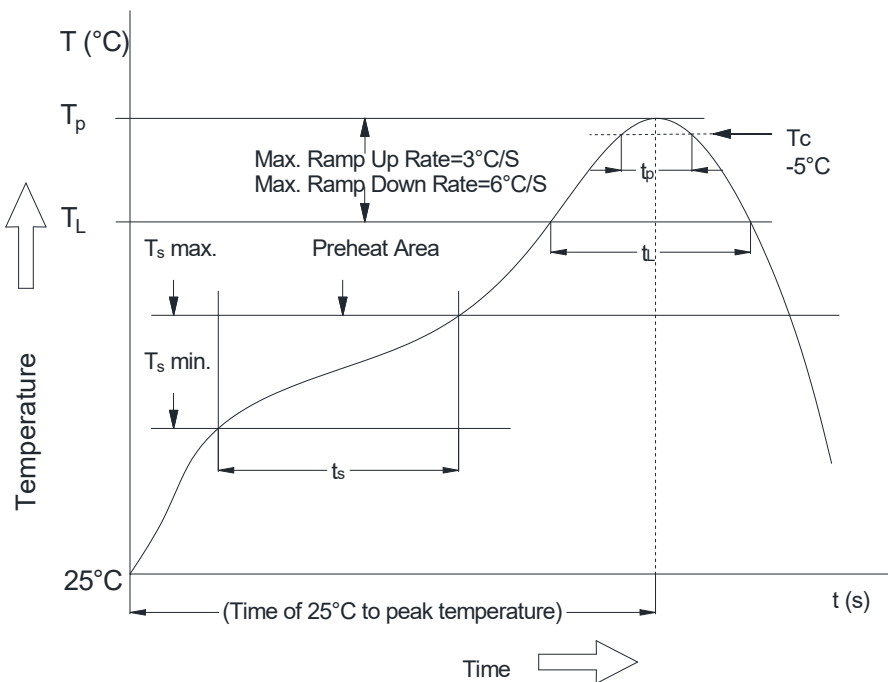
① Pre-heating should be in such a way that the temperature difference between solder and product surface is limited to 150°C max. Cooling into solvent after soldering also should be in such a way that temperature difference is limited to 100°C max. Unwrought pre-heating may cause cracks on the product, resulting in the deterioration of products quality.

② Standard soldering profile.



(3) Reflow soldering conditions

Classification Reflow Profile for SMT components:



Classification Reflow Soldering Profile:

Profile Feature		Lead-Free Assembly
Average Ramp-Up Rate (Ts max. to Tp)		3°C/second max.
Preheat	- Temperature Min (Ts min.)	150°C
	- Temperature Max (Ts max.)	200°C
	- Time (Ts min to Ts max.)	60-120 seconds
Time maintained above	- Temperature (TL)	217°C
	- Time (tL)	60-150 seconds
Peak/Classification Temperature (Tp)		see table below
Peak/Classification Time (Tp)		3-4 seconds
Time within 5 °C of actual Peak Temperature (tp)		20-30 seconds
Ramp-Down Rate		6°C/second max.
Time 25 °C to Peak Temperature		8 minutes max.

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

2: Refer to IPC/ JEDEC J-STD-020E

Package Classification Reflow Temperature:

Properties	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly Package Thickness < 1.6 mm	260°C	260°C	260°C
PB-Free Assembly Package Thickness 1.6mm-2.5 mm	260°C	250°C	245°C
PB-Free Assembly Package Thickness ≥2.5 mm	250°C	245°C	245°C

Refer to IPC/ JEDEC J-STD-020E

(4) The method on Re-work with using the iron:

The following conditions must be strictly followed when using a soldering iron

Pre-heating	150°C, 1 minute
Tip temperature	280°C max
Soldering iron output	20w max
End of soldering iron	φ1mm max
Soldering time	3 seconds max

Products Storage

(1) Storage period

Products which inspected in LYEC over 12 months ago should be examined and used, which can be confirmed with inspection No. marked on the container. Solderability should be checked if this period is exceeded.

(2) Storage conditions

Products should be storage in the warehouse on the following conditions:

Temperature: Less than 40°C

Humidity : Less than 75% relative and humidity

No rapid change on temperature and humidity

- (3) Don't keep products in corrosive gases such as sulfur, chlorine gas or acid, or it may cause oxidization of electrode, resulting in poor solderability.
- (4) Products should be storage on the palette for the prevention of the influence from humidity, dust and so on.
- (5) Products should be storage in the warehouse without heat shock, vibration, direct sunlight and so on.
- (6) Products should be storage under the airtight packaged condition.